

DESCRIPTION

PT2272 is a remote control decoder paired with PT2262 utilizing CMOS Technology. It has 12-bit of tri-state address pins providing a maximum of 531,441 (or 312) address codes; thereby, drastically reducing any code collision and unauthorized code scanning possibilities. PT2272 is available in several options to suit every application need: variable number of data output pins, latch or momentary output type.

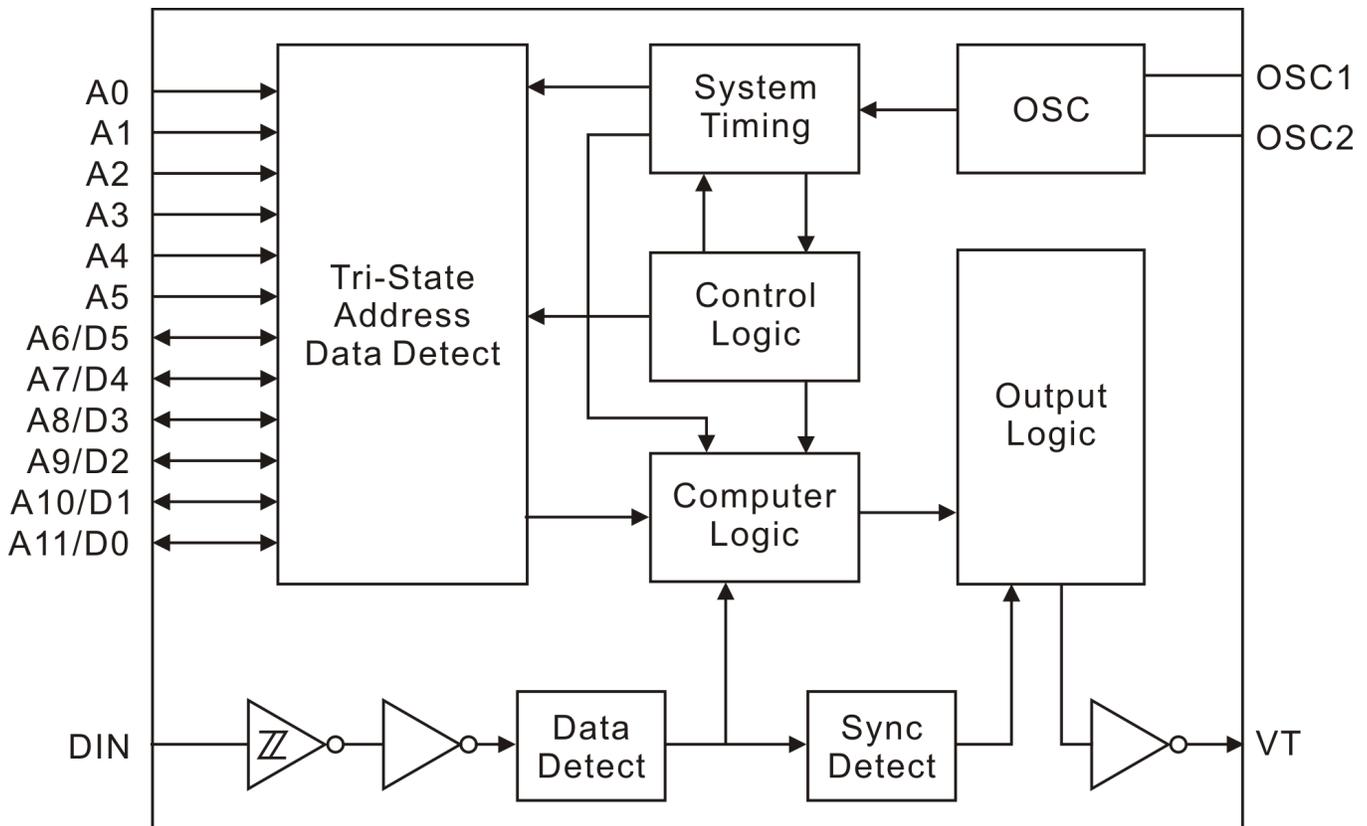
FEATURES

- CMOS Technology
- Low Power Consumption
- Very High Noise Immunity
- Up to 12 Tri-State Code Address Pins
- Up to 6 Data Pins
- Wide Range of Operating Voltage: VCC=4~15V
- Single Resistor Oscillator
- Latch or Momentary Output Type
- Available in DIP and SOP

APPLICATIONS

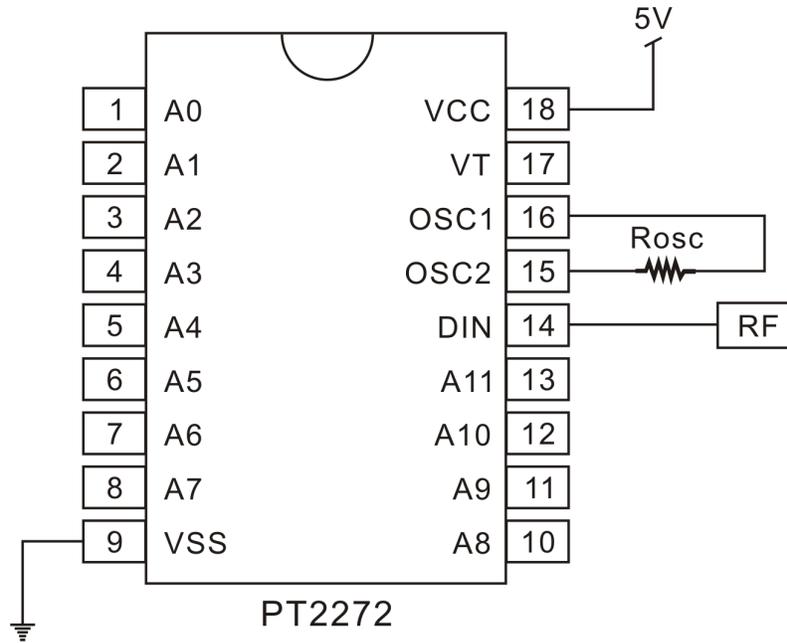
- Car Security System
- Garage Door Controller
- Remote Control Fan
- Home Security/Automation System
- Remote Control Toys
- Remote Control for Industrial Use

BLOCK DIAGRAM

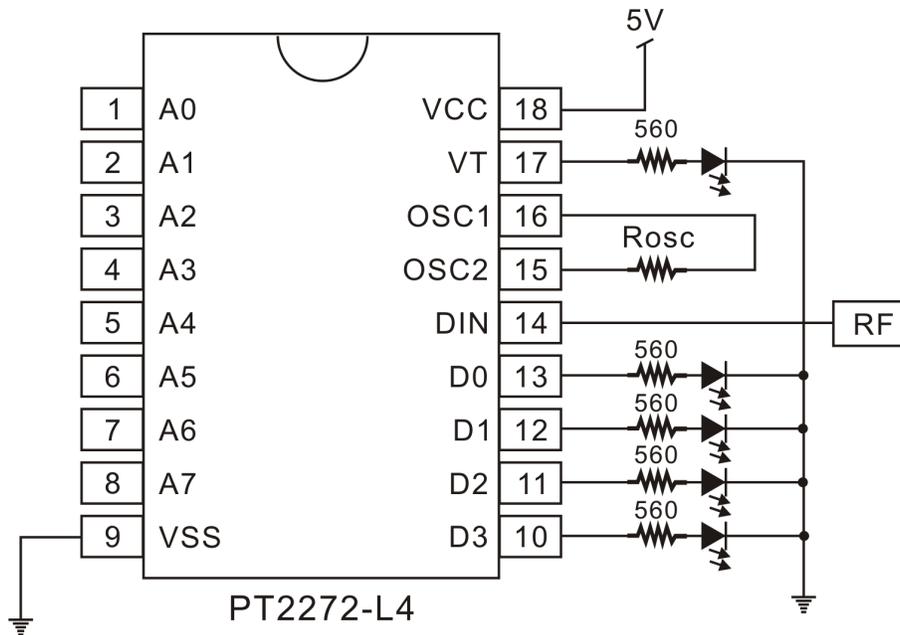


APPLICATION CIRCUIT

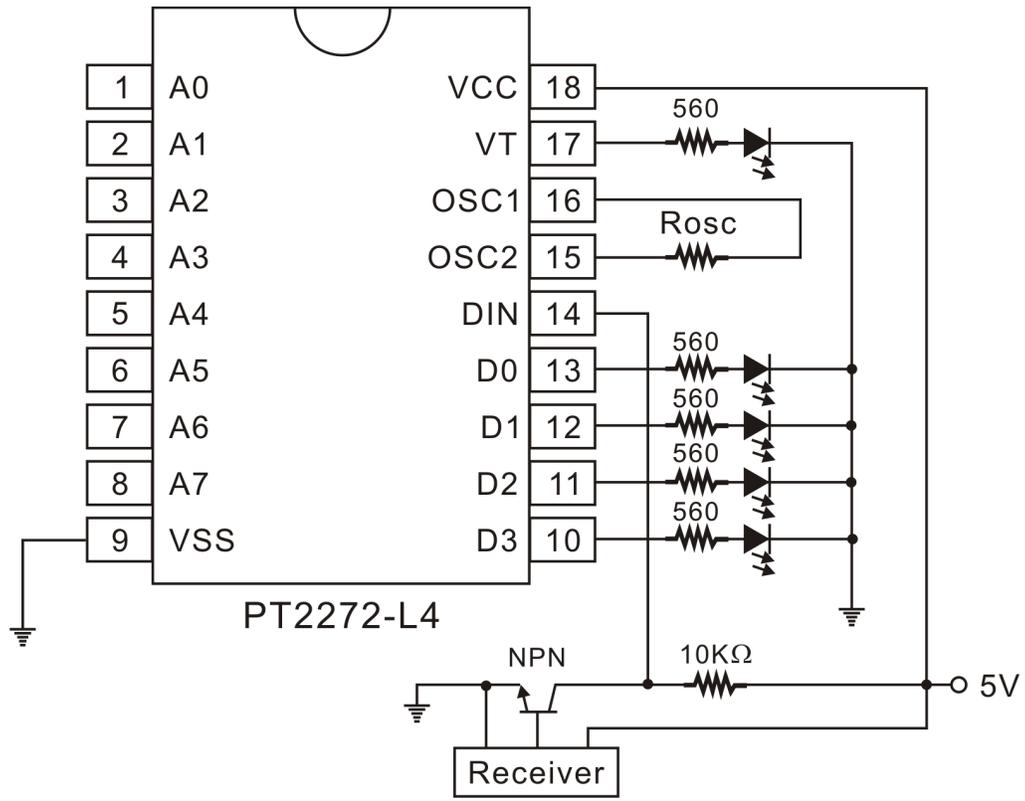
PT2272 (NO DATA) RF APPLICATION



PT2272 (4 DATA) RF APPLICATION CIRCUIT



PT2272 (4 DATA) IR APPLICATION CIRCUIT





ORDER INFORMATION

Valid Part No.	Package Type	Data Bits	Output Type	Top Code
PT2272A-M2-S18	18 PINS, SOP	2	Momentary	PT2272

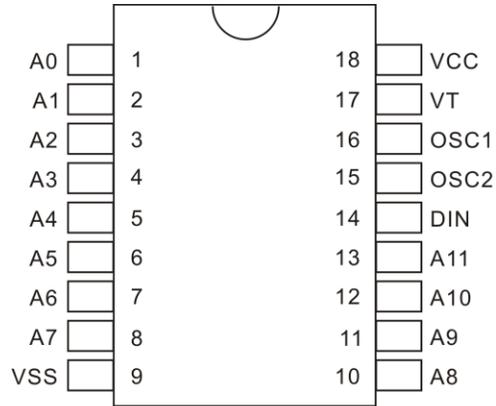
Valid Part No.	Package Type	Data Bits	Output Type	Top Code
PT2272	18 PINS, DIP	-	*	PT2272
PT2272-L3	18 PINS, DIP	3	Latch	PT2272-L3
PT2272-L4	18 PINS, DIP	4	Latch	PT2272-L4
PT2272-L5	18 PINS, DIP	5	Latch	PT2272-L5
PT2272-M2	18 PINS, DIP	2	Momentary	PT2272-M2
PT2272-M3	18 PINS, DIP	3	Momentary	PT2272-M3
PT2272-M4	18 PINS, DIP	4	Momentary	PT2272-M4
PT2272-M5	18 PINS, DIP	5	Momentary	PT2272-M5
PT2272-M6	18 PINS, DIP	6	Momentary	PT2272-M6
PT2272A-M2	18 PINS, DIP	2	Momentary	PT2272A-M2

Valid Part No.	Package Type	Data Bits	Output Type	Top Code
PT2272-S	20 PINS, SOP	-	*	PT2272-S
PT2272-L3S	20 PINS, SOP	3	Latch	PT2272-L3S
PT2272-L4S	20 PINS, SOP	4	Latch	PT2272-L4S
PT2272-L5S	20 PINS, SOP	5	Latch	PT2272-L5S
PT2272-L6S	20 PINS, SOP	6	Latch	PT2272-L6S
PT2272-M2S	20 PINS, SOP	2	Momentary	PT2272-M2S
PT2272-M3S	20 PINS, SOP	3	Momentary	PT2272-M3S
PT2272-M4S	20 PINS, SOP	4	Momentary	PT2272-M4S
PT2272-M5S	20 PINS, SOP	5	Momentary	PT2272-M5S
PT2272-M6S	20 PINS, SOP	6	Momentary	PT2272-M6S
PT2272A-L2S	20 PINS, SOP	2	Latch	PT2272A-L2S
PT2272A-M2S	20 PINS, SOP	2	Momentary	PT2272A-M2S

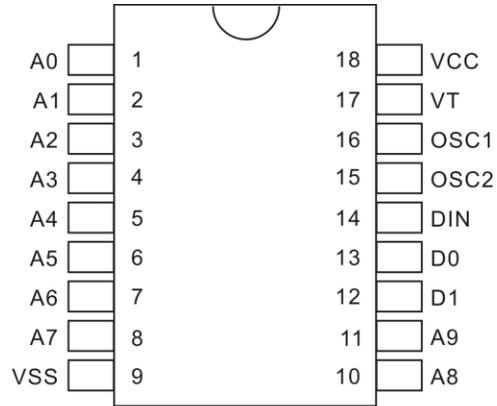
Note: *=use VT (Valid Transmission).

PIN CONFIGURATION

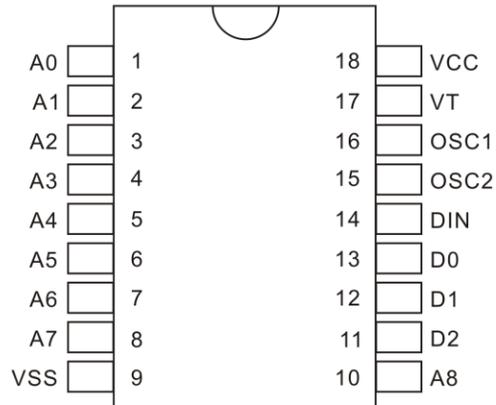
18 PINS



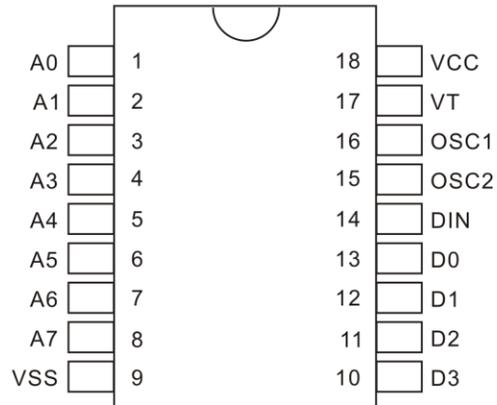
PT2272



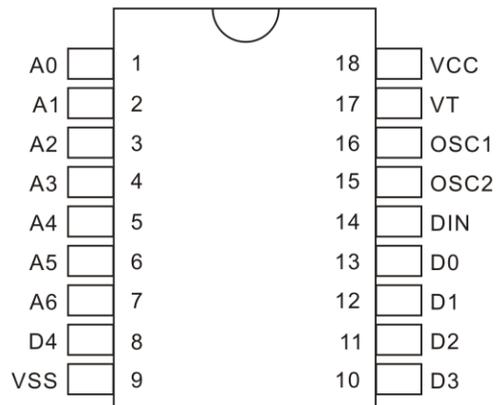
PT2272A-M2



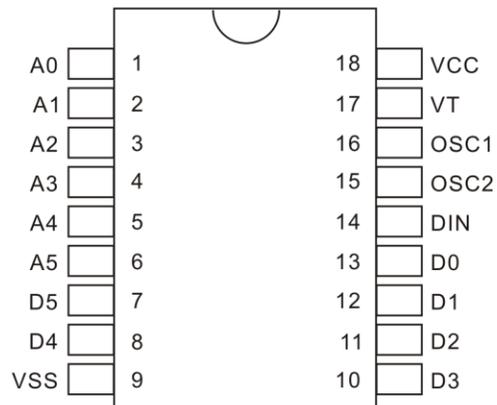
PT2272-M3/L3



PT2272-M4/L4

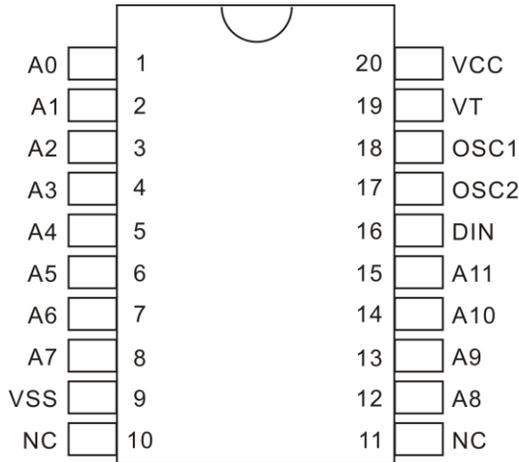


PT2272-M5/L5

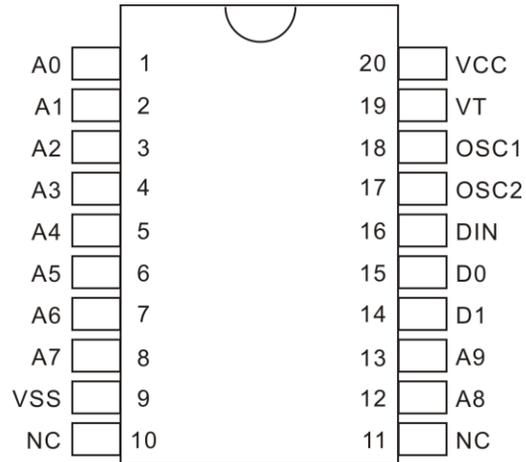


PT2272-M6

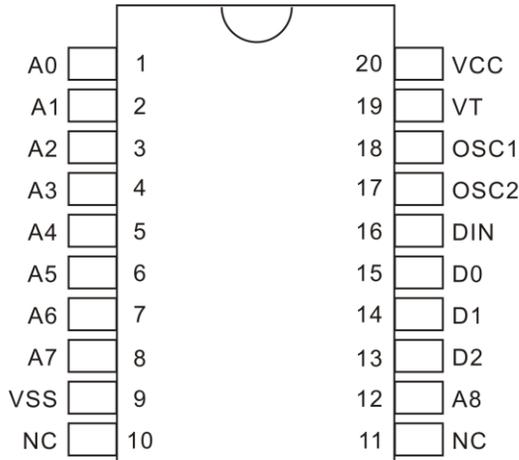
20 PINS



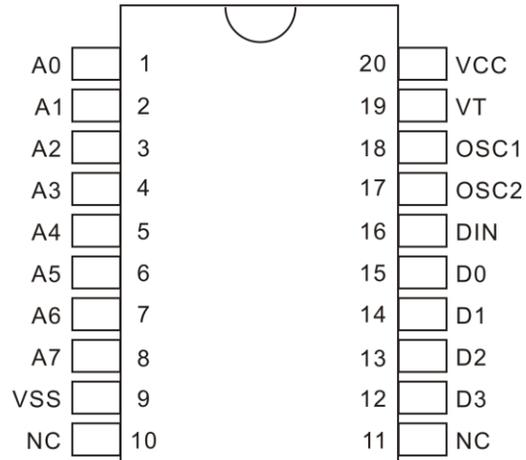
PT2272-S



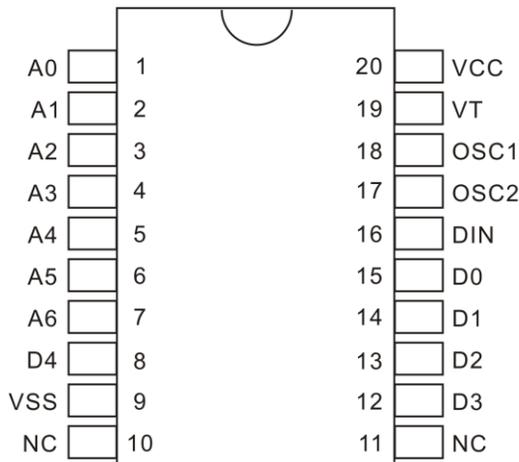
PT2272A-M2S/L2S



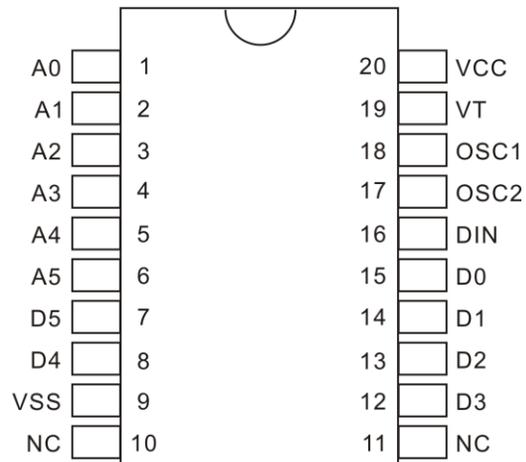
PT2272-M3S/L3S



PT2272-M4S/L4S



PT2272-M5S/L5S



PT2272-M6S/L6S

PIN DESCRIPTION

Pin Name	I/O	Description	Pin No.	
			18 pins	20 pins
A0 ~ A5	I	Code Address Pin Nos.0 ~ 5. These six tri-state pins are detected by PT2272 to determine the encoded waveform bit 0 ~ bit 5. Each pin can be set to "0", "1", or "f" (floating).	1 ~ 6	1 ~ 6
A6/D5 ~ A11/D0	I/O	Code Address Pin Nos.6 ~ 11/Data Pin Nos.5 ~ 0. These six pins are used as higher address input bits or data output pins depending on the version (type) of PT2272 used. When used as address inputs, these pins are tri-state input pins and each pin can be set to "0", "1", or "f" (floating). When used as output pins, these pins are driven to VCC if (1) the address decoded from the waveform that was received matches the address setting at the address input pins, and (2) the corresponding data bits received is a "1" bit. Otherwise, they are driven to VSS.	7 ~ 8 10 ~ 13	7 ~ 8 12 ~ 15
VSS	-	Negative Power Supply	9	9
NC	-	No Connection	-	10 ~ 11
DIN	I	Data Input Pin. The encoded waveform received is serially fed to PT2272 at this pin.	14	16
OSC 1	I	Oscillator Pin No.1	15	17
OSC 2	O	Oscillator Pin No.2	16	18
VT	O	Valid Transmission. Active High Signal. VT in high state signifies that PT2272 receives valid transmission waveform.	17	19
VCC	-	Positive Power Supply	18	20

IMPORTANT NOTICE

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